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August 15, 2005

TO: : Examiner - Vikki H. Trinh FAX : (571) 273-1719
Group: 2814
Assistant Commissioner for Patents Telephone : (571) 272-1719
Washington, DC 20231

FROM: : Walter Fields Telephone (360) 750-9936
Reference No.: : Ser. No.: 10/639,058 Fax (360) 838-0144
Filed: Aug 11, 2003
Inv.: Andrew Horch
Atty Dkt: 2000.004.00/US

Number of Pages (including this cover sheet) : 4

Comments:

Responsive to an inquiry of this morning, please find a facsimile submission for a supplemental reply.

Feel free to contact us with any questions.

Walter D. Fields


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Andrew E. Horch	§	Group Art Unit: 2814
Serial No.: 10/639,058	§	Examiner: Vikki H. Trinh
Filed: August 11, 2003	§	Confirmation No. 6760
Title: Thyristor Semiconductor Memory	§	Atty. Docket: 2000.004.00/US
Device and Method of Manufacture	§	Date: August 15, 2005

SUPPLEMENTAL AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

<p>CERTIFICATE OF FACSIMILE SUBMISSION 37 CFR 1.8 I hereby certify that this correspondence is being facsimile transmitted to the United States Postal Service to Fax No. 571-273-1719, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on <u>August 15, 2005</u>.</p> <p>_____ Walter D. Fields</p>
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Dear Sir:

Applicant submits this amendment responsive to the Examiner's informality inquiry of earlier today and supplemental to applicant's previous amendment of June 20, 2005, which was submitted responsive to the Office Action dated March 30, 2005.

Amendment to the Specification begins on page 2.

Remarks/Arguments begin on page 3.